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Exam : **HPE0-J79**

Title : Architecting Multi-Site HPE
Storage Solutions

Version : DEMO

1.You are planning an HPE StoreVirtual architecture based on a customer's physical site configuration. The customer has three physical sites and wants to span the cluster between two of them. All sites should be peers.

What should be done to meet the requirements?

- A. Configure a Failover Manager in the third physical site.
- B. Configure the cluster to span both sites, and configure one site as they primary site.
- C. Create a logical third site to provide a Failover Manager.
- D. Configure a Failover Manager in each physical site.

Answer: A

2.What happens during the fabric login (FLOGI) during the Fibre Channel registration process? (Select two.)

- A. Service parameters are exchanged
- B. Buffer-to-bufler credits are initiated
- C. E_Port FCJDs are assigned
- D. F_Port error counters are cleared.
- E. Connections between N-Ports are established

Answer: A,E

3.You are designing an HPE 3PAR StoreServ environment using HPE Smart SAN Which solution components are supported? (Select two.)

- A. EmulexHBAs
- B. HPE 5900 CP switches
- C. Qlogic Fibre Channel HBAs
- D. HPE H-series SAN switches
- E. Physical 3PAR Service Processor

Answer: B,C

4.You are proposing an HPE 3PAR StoreServ solution to a customer who contacted you because of complaints received about the scalability of the company's current solution. The customer has concerns about implementing a new platform from a different vendor and the costs for professional services
What should you mention to the customer to address this concern?

- A. Thin Persistence
- B. Thin Provisioning
- C. hardware-assisted data migration
- D. support for SATA drives

Answer: C

5.You are designing a Fibre Channel storage solution for a customer who is currently attaching all hosts through the Ethernet and the TCP/IP protocol. What is an advantage of choosing Fibre Channel over TCP/IP?

- A. Less costly networking hardware
- B. Support for greater distances
- C. Less overhead

D. More robust error correction

Answer: D